

Arasan Chip Systems Announces Highly Integrated 10G Ethernet MAC IP Core

SAN JOSE, Calif., June 22, 2010 (GLOBE NEWSWIRE) – Arasan Chip Systems, Inc. (“Arasan”), a leading provider of Total Semiconductor IP Solutions, announced the release of its 10 Gigabit Ethernet MAC IP core. 10 Gigabit (10G) Ethernet boosts network bandwidth, thereby enabling the delivery of next generation applications over a common Ethernet platform. This high quality IP expands Arasan’s growing portfolio of Ethernet and Connectivity IP cores.

Arasan’s 10 Gigabit Ethernet MAC IP Core is compliant with the IEEE 802.3-2008 standard and incorporates a powerful 64-bit scatter-gather DMA to optimize network stack performance. All of the key functions of the Ethernet protocol such as IPG checking, address filtering, FCS and PAD processing, packet error checking are done in hardware thereby minimizing the burden on the host processor.

Commenting on the release of this IP, Prakash Kamat, Vice-President of Engineering at Arasan said, “Networking ASIC and chipset designers can capitalize on the rising demand for 10G Ethernet deployments by using Arasan’s highly optimized 10G Ethernet MAC IP core. Designers can rely on our extensive Ethernet protocol expertise to achieve success in integrating 10G Ethernet on their silicon.”

With Service Providers and Enterprise IT managers standardizing on an Ethernet based network infrastructure, 10G Ethernet enables them to replace multiple networks while increasing the bandwidth and lowering operating costs. The higher bandwidth also enables Enterprise wide virtualization, resulting in more efficient use of compute resources. Designers of Host-bus adapters, server connectivity and advanced storage applications can offer robust chipsets using Arasan’s proven 10G Ethernet MAC IP core.

Arasan's 10G Ethernet MAC IP core supports full-duplex operation at 10Gbps and all standard features such MDIO/MDC PHY management, MIB, SNMP, RMON network management. It supports MagicPacket and WakeonLAN to conserve power. The core also supports PAUSE frame management to throttle output based on network congestion. Arasan also offers two optional modules – a XAUI Interface module that extends the XGMII interface to a XAUI interface and an optional 64B/66B PCS module to interface with a 10G SerDes module.

“10G Ethernet is increasingly being adopted as the technology that scales across LAN, MAN and WAN connectivity,” said Somnath Viswanath, Product Marketing Manager at Arasan. “By integrating Arasan's proven 10G Ethernet MAC IP core, designers can offer chipsets that handle data and bandwidth intensive applications such as large corporate database access, enterprise data backup and HD media using a common Ethernet infrastructure”.

Arasan provides a Total IP solution to enable the rapid integration of 10G Ethernet interface into chipsets and network processors. This solution consists of RTL code, verification IP, software stacks, hardware platforms for development and validation – all backed by our World-class customer support.